

FORM PTO-892 (REV. 3-78)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <b>843454</b>	GROUP/ART UNIT <b>114</b>	ATTACHMENT TO PAPER NUMBER <b>6</b>
NOTICE OF REFERENCES CITED				APPLICANT(S) <b>Blanchard.</b>		

  

U.S. PATENT DOCUMENTS																	
*	A	B	C	D	E	F	G	H	I	J	K	DOCUMENT NO.	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE
	A											4520552	6-85	Armoucl	29	580	
	B											4364074	12-82	Garmache et al.	148	188	
	C											<del>4554728</del>	<del>12-85</del>	<del>SHEPARD</del>	<del>148</del>	<del>019.50</del>	
	D											4353086	10-82	Jaccodine et al.	29	580	
	E											4455740	6-84	Iwai	148	019.50	
	F											3518509	6-70	Cullis	357	23.4	
	G											4631803	12-86	Hunter et al.	29	578	
	H											3412297	11-68	Ammlinger	357	23.4	
	I											4571815	2-86	Baliga et al.	29	578	
	J											4528047	7-85	Beyer et al.	148	175	
	K											3500139	3-70	Frumm et al.	148	019.50	

  

FOREIGN PATENT DOCUMENTS														
*	L	M	N	O	P	Q	DOCUMENT NO.	DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG.	PP. SPEC.
	L						0164095	12-85	EP	Benjamin	357	23.4		
	M						0003287	1-83	Japan	Furumura	357	23.4		
	N						0050752	3-83	Japan	Fukuyama	29	576		
	O													
	P													
	Q													

  

OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)	
R	Ghandhi, "VLSI Fabrication principles", John Wiley & Sons, N.Y. 1983 pp. 582-585
S	
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U	

  

EXAMINER <b>T. Thomas</b>	DATE <b>6-9-87</b>	
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\* A copy of this reference is not being furnished with this office action.  
(See Manual of Patent Examining Procedure, section 707.05 (a).)